PATENT ABSTRACTS OF JAPAN

(11)Publication number:

59-194563

(43) Date of publication of application: 05.11.1984

•(51)Int.CI.

HO4N 1/04 HO4N 1/00

(21)Application number: 58-070451

(22)Date of filing:

: 58-070451 19.04.1983 (71)Applicant:

MITSUBISHI ELECTRIC CORP

(72)Inventor:

NAGATA YOSHIHIRO

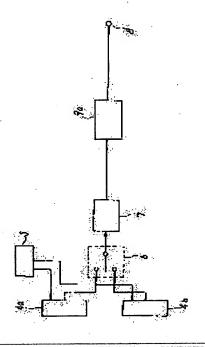
MITSUDA HIROSHI

(54) ORIGINAL READER

(57)Abstract:

PURPOSE: To read out pictures at a high speed and to prevent the read pictures from deterioration by scanning an original at the same timing as plural image sensors and inputting the read signals to one signal line with time division in each picture element.

CONSTITUTION: Two read signals obtained from the image sensors 4a, 4b are multiplied by a signal switching circuit 6 with time division on the basis of a control signal from a scanning control circuit 5. These read signals are applied to a signal processing circuit 7 to be level-controlled or binary coded, separated and combined in series again by a signal composing circuit 9a and then outputted as picture signals for one line's share.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office